# SINGLE-SUPPLY DUAL OPERATIONAL AMPLIFIER

#### GENERAL DESCRIPTION

The NJM2904 consists of two independent, high gain, internally frequency compensated operation amplifiers, which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks, and all the conventional op amp circuits, which now can be more easily implemented in single power supply systems. For example, the NJM2904 can be directly operated off of the standard +5V power supply voltage, which is used in digital systems and will easily provide the required interface electronics without requiring the additional ±15V power supplies.

### ■ FEATURES

- Single Supply
- Operating Voltage +3V~+32V
- Low Operating Current 0.7mA typ.
- Slew Rate 0.5V/µs typ.
- Bipolar Technology
- Package Outline

DIP8, DMP8, SSOP8, SIP8, SOP8 JEDEC 150mil MSOP8 (VSP8) MEET JEDEC MO-187-DA MSOP8 (TVSP8) MEET JEDEC MO-187-DA/THIN TYPE

#### PIN CONFIGURATION



### ■ PACKAGE OUTLINE



NJM2904D



NJM2904M



NJM2904E

NJM2904R/RB1

NJM2904V



NJM2904L

# ■ ABSOLUTE MAXIMUM RATINGS

			( Ta=25°C )
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V^{+}(V^{+}N^{-})$	32 ( or ±16 )	V
Differential Input Voltage	V <sub>ID</sub>	32	V
Input Voltage	V <sub>IC</sub>	-0.3~+32 (note 2)	V
Power Dissipation	PD	(DIP8) 500 (DMP8) 300 (EMP8) 300 (SSOP8) 250 (MSOP8(VSP8/TVSP8)) 320 (SIP8) 800	mW
Operating Temperature Range	T <sub>opr</sub>	-40~+85	°C
Storage Temperature Range	T <sub>stg</sub>	-50~+125	°C

(note 1) Continuous short-circuits from output to GND is guaranteed only when  $V^{^{+}}\!\leq\!15V\!.$ 

(note 2) For supply voltage less than 32V, the absolute maximum input voltage is equal to the supply voltage.

# ■ ELECTRICAL CHARACTERISTICS

					( Ta=25°C,V⁺=5V )		
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Input Offset Voltage	V <sub>IO</sub>	R <sub>s</sub> =0Ω	-	2	7	mV	
Input Offset Current	lio		-	5	50	nA	
Input Bias Current	IB		-	25	250	nA	
Large Signal Voltage Gain	Av	R <sub>L</sub> ≥2kΩ	-	100	-	dB	
Maximum Output Voltage Swing	VOPP	R <sub>L</sub> =2kΩ	3.5	-	-	V	
Input Common Mode Voltage Range	VICM		0~3.5	-	-	V	
Common Mode Rejection Ratio	CMR		-	85	-	dB	
Supply Voltage Rejection Ratio	SVR		-	100	-	dB	
Output Source Current	ISOURCE	V <sub>IN</sub> <sup>+</sup> =1V,V <sub>IN</sub> <sup>-</sup> =0V	20	30	-	mA	
Output Sink Current	ISINK	V <sub>IN</sub> <sup>+</sup> =0V,V <sub>IN</sub> <sup>-</sup> =1V	8	20	-	mA	
Channel Separation	CS	f=1k~20kHz,Input Referred	-	120	-	dB	
Operating Current	Icc	R <sub>L</sub> =∞	-	0.7	1.2	mA	
Slew Rate	SR	V <sup>+</sup> /√=±15V	-	0.5	-	V/µs	
Unity Gain Bandwidth	f <sub>T</sub>	V <sup>+</sup> /√=±15V	-	0.6	-	MHz	

### ■ TYPICAL CHARACTERISTICS



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## ■ APPLICATION

Improvement of Cross-over Distortion Equivalent circuit at the output stage



NJM2904,in its static state ( No in and output condition ) when design,  $Q_U\,$  being biassed by constant current ( break down beam ) yet,  $Q_L\,$  stays OFF.

While using with both power source mode, the cross-over distortion might occur instantly when  $Q_L$  ON.

There might be cases when application for amplifier of audio signals,not only distortion but also the apparent frequency bandwidth being narrowed remarkably.

It is adjustable especially when using both power source mode, constantly to use with higher current on  $Q_U$  than the load current (including feedback current), and then connect the pull-down resister  $R_P$  at the part between output and V<sup>-</sup> pins.



