SGM8193 Tiny Packages, Nano-Power, Precision Current-Sense Amplifier

GENERAL DESCRIPTION

The SGM8193 series is a nano-power, high precision, high-side current-sense amplifier. The device consumes only 1.1μ A (MAX) quiescent current. It features a maximum 100μ V low offset voltage, which enables 25mV to 50mV full-scale drops when sensing full current. It has a gain error less than 0.5%. The device can sense the voltage across a current-sense resistor at common mode voltages from 1.6V to 28V. The SGM8193 series provides four fixed gains: 25V/V, 50V/V, 100V/V and 200V/V, which allows flexible selection of the external current-sense resistor.

The SGM8193 is available in Green SOT-23-5 and WLCSP-1×1-4B packages. The tiny packages make the device a good choice for portable and battery-powered applications where limited size of PCB is the limitation. It is rated over the -40 $^{\circ}$ C to +125 $^{\circ}$ C temperature range.

FEATURES

- Ultra-Low Quiescent Current at T_A = +25°C: 0.8μA (TYP), 1.1μA (MAX)
- Input Common Mode Range: 1.6V to 28V
- Low Input Offset Voltage: 100µV (MAX)
- Choice of Gains:
 - SGM8193A0 Gain: 25V/V
 - SGM8193A1 Gain: 50V/V
 - SGM8193A2 Gain: 100V/V
 - SGM8193A3 Gain: 200V/V
- Low Gain Error: 0.5% (MAX)
- Voltage Output
- -40°C to +125°C Operating Temperature Range
- Available in Green SOT-23-5 and WLCSP-1×1-4B Packages

APPLICATIONS

Portable Equipment Battery-Powered Equipment Cell Phones Notebook PCs PDAs Power Management

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM8193A0	SOT-23-5	-40°C to +125°C			
(Gain = 25V/V)	WLCSP-1×1-4B	-40°C to +125°C			
SGM8193A1	SOT-23-5	-40°C to +125°C	SGM8193A1XN5G/TR	MF5XX	Tape and Reel, 3000
(Gain = 50V/V)	WLCSP-1×1-4B	-40°C to +125°C	SGM8193A1XG/TR	5G XX	Tape and Reel, 3000
SGM8193A2	SOT-23-5	-40°C to +125°C			
(Gain = 100V/V)	WLCSP-1×1-4B	-40°C to +125°C			
SGM8193A3 (Gain = 200V/V)	SOT-23-5	-40°C to +125°C			
	WLCSP-1×1-4B	-40°C to +125°C			

MARKING INFORMATION



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

RS+, RS- to GND	0.3V to +30V
OUT to GND	0.3V to +6V
RS+ to RS	±30V
Short-Circuit Duration, OUT to GND	Continuous
Continuous Input Current (any pin)	±20mA
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	
HBM	TBD
CDM	TBD

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range-40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to

absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATIONS





PIN DESCRIPTION

PI	PIN		FUNCTION	
WLCSP-1×1-4B	SOT-23-5	NAME	FUNCTION	
A1	5	RS+	Connect to the supply-side of the external current-sense resistor.	
A2	4	RS-	Connect to the load-side of the external current-sense resistor.	
B1	1, 2	GND	Ground.	
B2	3	OUT	Output Voltage. V_{OUT} and $V_{SENSE} = V_{RS+} - V_{RS-}$ are in direct proportion.	

ELECTRICAL CHARACTERISTICS

 $(V_{RS+} = V_{RS-} = 3.6V, V_{SENSE} = (V_{RS+} - V_{RS-}) = 0V, Full = -40^{\circ}C$ to +125°C, typical values are at T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		TEMP	MIN ⁽¹⁾	TYP	MAX ⁽¹⁾	UNITS
Input Characteristics	•				•		•	
Input Offset Voltage ⁽²⁾	V _{os}			+25°C		3		μV
Input Common Mode Voltage Range	V _{CM}	Guaranteed by CMRR	+25°C	1.6		28	V	
Common Mode Rejection Ratio	CMRR	1.6V < V _{RS+} < 28V		+25°C		130		dB
Output Characteristics								
Gain		SGM8193A0		+25°C		25		- V/V
		SGM8193A1		+25°C		50		
	G	SGM8193A2		+25°C		100		
		SGM8193A3		+25°C		200		
Gain Error ⁽³⁾	GE			+25°C		±0.1		%
		SGM8193A0/SGM8193A1/SGM8193A2		Full		10		kΩ
Output Resistance ⁽⁴⁾	Rout	SGM8193A3		Full		20		
		G = 25, SGM8193A0		+25°C		0.05		- mV
	V _{OL}	G = 50, SGM8193A1		+25°C		0.1		
Low Output Voltage		G = 100, SGM8193A2		+25°C		0.2		
		G = 200, SGM8193A3		+25°C		0.4		
High Output Voltage ⁽⁵⁾	V _{OH}	V _{OH} = V _{RS-} - V _{OUT}	SGM8193A0/ SGM8193A1/ SGM8193A2	Full		0.1		V
			SGM8193A3	Full		0.06		
Dynamic Performance			·	•			•	•
	BW	V _{SENSE} = 100mV, G = 25		+25°C		TBD		
Creall Circal Dandwidth ⁽⁴⁾		V _{SENSE} = 50mV, G = 50		+25°C		300		kHz
Small-Signal Bandwidth ⁽⁴⁾		V _{SENSE} = 25mV, G = 100		+25°C		225		
		V _{SENSE} = 12.5mV, G = 200		+25°C		150		
Output Settling Time	ts	1% final value, V _{SENSE} = 100mV, SGM8193A0		+25°C		TBD		- µs
		1% final value, V _{SENSE} = 50mV, SGM8193A1		+25°C		10		
		1% final value, V _{SENSE} = 25mV, SGM8193A2		+25°C		20		
		1% final value, V _{SENSE} = 12.5mV, SGM8193A3		+25°C				
Power Supply					•	•	-	•
Supply Current (6)	I _{cc}	1.6V < V _{RS+} < 28V		+25°C		0.8		μA

NOTES:

1. Specified by design and characterization; not production tested.

2. V_{OS} is inferred from the measured value of gain error test.

3. Gain error is the difference between the ideal gain and the gain obtained by calculating two V_{SENSE} measured values.

- G = 25, $V_{SENSE} = 20mV$ and 120mV.
- G = 50, V_{SENSE} = 10mV and 60mV.
- G = 100, V_{SENSE} = 5mV and 30mV.
- G = 200, V_{SENSE} = 2.5mV and 15mV.

4. The device can keep stable with all external capacitance values.

5. V_{OH} is defined as the voltage difference between V_{RS-} and V_{OUT} , under the corresponding gain when $V_{SENSE} = V_{RS+} - V_{RS-} = 3.6V$.

6. I_{CC} is defined as the total current of I_{RS^+} and I_{RS^-} when V_{OUT} = 0.

PACKAGE OUTLINE DIMENSIONS

SOT-23-5





RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters				
	MIN	MOD	MAX		
А	-	-	1.450		
A1	0.000	-	0.150		
A2	0.900	-	1.300		
b	0.300	-	0.500		
С	0.080	-	0.220		
D	2.750	-	3.050		
E	1.450	-	1.750		
E1	2.600	-	3.000		
е	0.950 BSC				
e1	1.900 BSC				
L	0.300	_	0.600		
θ	0°	_	8°		
ссс	0.100				

NOTES:

1. This drawing is subject to change without notice.

2. The dimensions do not include mold flashes, protrusions or gate burrs.

3. Reference JEDEC MO-178.

PACKAGE OUTLINE DIMENSIONS

WLCSP-1×1-4B





Symbol	Dimensions In Millimeters				
	MIN	MOD	MAX		
А	0.602	0.640	0.678		
A1	0.216	0.236	0.256		
D	0.970	1.000	1.030		
E	0.970	1.000	1.030		
d	0.299	0.319	0.339		
е	0.500 BSC				
ccc	0.050				

NOTE: This drawing is subject to change without notice.