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HA17358A Series

Dual Operational Amplifier

REJ03D0676-0400 Rev.4.00 Mar 10, 2006

Description

HA17358A series are dual operational amplifier that provide high gain and internal phase compensation, with single power supply. They can be widely applied to control equipments and to general use.

Features

- Wide range of supply voltage, and single power supply used
- Wide range of common mode voltage, and possible to operate with an input about 0 V, and output around 0 V is available
- Frequency characteristics and input bias current are temperature compensated
- Low electro-magnetic susceptibility level



Ordering Information

Type No.	Application	Package Name	Package Code
HA17358A	Commercial use	DIP-8 pin	PRDP0008AF-B
HA17358AF		SOP-8 pin (JEITA)	PRSP0008DE-B
HA17358ARP		SOP-8 pin (JEDEC)	PRSP0008DD-C
HA17358AT		TSSOP-8 pin	PTSP0008JC-B

Pin Arrangement



Circuit Schematic (1/2)



Note: If Input/Output terminals voltage over the absolute maximum ratings, there is possibility of mis-operation, characteristics deterioration and destruction, because of the current's flowing to parasitic diode in IC. The Input/Output terminals are recommended to be protected with the clamp circuit which using the diode with low forward voltage (like schottky barrier diode) when there is a possibility for the Input/Output terminals voltage exceeds the absolute maximum ratings.

Absolute Maximum Ratings

				$(1a = 25^{\circ}C)$
Item		Symbol	Ratings	Unit
Power supply voltage		V _{CC}	32	V
Output sink current		losink	50	mA
Common mode input voltage		V _{CM}	–0.3 to +V _{CC}	V
Differential input voltage		Vin(diff)	±V _{CC}	V
Output voltage		Vout	–0.3 to +V _{CC}	V
Allowable power dissipation	DIP	PT	570 * ¹	mW
	SOP		385 * ²	
	TSSOP		192 * ³	
Operating temperature		Topr	-40 to +85	°C
Storage temperature		Tstg	–55 to +125	°C

 $(T_{0} - 25^{\circ}C)$

Notes: 1. HA17358A:

This is the allowable values up to $Ta = 50^{\circ}C$. Derate by 8.3 mW/°C.

2. HA17358AF/ARP:

These are the allowable values up to $Ta = 25^{\circ}C$ mounting in air.

When it is mounted on glass epoxy board of 40 mm \times 40 mm \times 1.5 mmt with 30% wiring density, the allowable value is 570 mW up to Ta = 45°C. If Ta > 45°C, derate by 7.14 mW/°C.

HA17358AT: These are the allowable values up to Ta = 25°C. Derate by 1.92 mW/°C above that temperature.

Electrical Characteristics

 $(V_{CC} = +15 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$ Unit **Test Conditions** Item Symbol Min Тур Max Input offset voltage 7 mV V_{CM} = 7.5V, R_S = 50 Ω , Rf = 50 $k\Omega$ V_{IO} ____ 3 $V_{CM} = 7.5V, \ I_{IO} = | \ I_{I \ (+)} - I_{I \ (-)} |$ Input offset current I_{IO} 5 50 nA 250 Input bias current 30 nA $V_{CM} = 7.5V$ I_{IB} _ Power source rejection ratio PSRR dB $R_S = 1k\Omega$, $Rf = 100k\Omega$ ____ 93 _____ Voltage gain A_{VD} 90 dB $R_L = \infty$, $R_S = 1k\Omega$, $Rf = 100k\Omega$ 75 CMR dB $R_S = 50\Omega$, $Rf = 5k\Omega$ Common mode rejection ratio ____ 80 $R_{\rm S} = 1 k\Omega$, $Rf = 100 k\Omega$ Common mode input voltage range V_{CM (+)} 13.5 V V $R_S = 1k\Omega$, $Rf = 100k\Omega$ V_{CM (-)} ____ _ -0.3 V $f = 100Hz, R_{L} = 20k\Omega, R_{S} = 1k\Omega,$ Peak-to-peak output voltage V_{OP-P} 13.6 ____ $Rf = 100k\Omega$ Output source current losource 20 40 mΑ $V_{IN}^{+} = 1V, V_{IN}^{-} = 0V, V_{OH} = 10V$ _ Output sink current 10 20 $V_{IN}^{-} = 1V, V_{IN}^{+} = 0V, V_{OL} = 2.5V$ losink mΑ ____ μA $V_{IN}^{-} = 1V, V_{IN}^{+} = 0V, Vout = 200mV$ 15 50 Supply current _ 0.8 2 $V_{IN} = GND, R_L = \infty$ I_{CC} mΑ SR 0.2 $R_L = \infty$, $V_{CM} = 7.5V$, f = 1.5kHzSlew rate V/µs Channel separation *1 CS (120)____ dB f = 1kHz____

Note: 1. Design spec.

Characteristic Curves





Package Dimensions





HA17358A Series





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